

F rm PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT <i>(Use several sheets if necessary)</i>			ATTORNEY DOCKET NO. AUS920030554US1		SERIAL NO. Not Assigned 10/757,249	
			APPLICANT DeWitt, Jr. et al.			
			FILING DATE 1/14/04		GROUP ART UNIT Not Assigned 2,185	
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE	
A.S.	AA 5,103,394	Apr. 7, 1992	Blasclak	395/575	Dec. 21, 1989	
A.S.	AB 6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999	
A.S.	AC 6,480,938 B2	Nov. 12, 2002	Vondran, Jr.	711/125	Dec. 15, 2000	
A.S.	AD 6,430,741 B1	Aug. 6, 2002	Mattson, Jr. et al.	717/154	Feb. 26, 1999	
A.S.	AE 6,189,141 B1	Feb. 13, 2001	Benitez et al.	717/4	May 4, 1998	
A.S.	AF 5,930,508	Jul. 27, 1999	Faraboschi et al.	395/706	Jun. 9, 1997	
A.S.	AG 6,351,844 B1	Feb. 26, 2002	Bala	717/4	Nov. 5, 1998	
A.S.	AH 6,324,689 B1	Nov. 27, 2001	Lowney et al.	717/9	Sep. 30, 1998	
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A.S.	AN 6,223,338 B1	Apr. 24, 2001	Smolders	717/4	Sep. 30, 1998	
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A.S.	AP 6,256,775 B1	Jul. 3, 2001	Flynn	717/4	Dec. 11, 1997	
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A.S.	BA 5,966,537	Oct. 12, 1999	Ravichandran	395/709	May 28, 1997	
DATE CONSIDERED 2/15/06			EXAMINER Arpan Sarka			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.						

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			APPLICANT DeWitt, Jr. et al.			
			FILING DATE <div style="font-size: 1.5em;">1/14/04</div>		GROUP ART UNIT Not Assigned <div style="font-size: 1.5em;">2185</div>	

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EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE	
A.S.	BB	2002/0129309	Sep. 12, 2002	Floyd et al.	714/724	Dec. 18, 2000
A.S.	BC	2001/0032305	Oct. 18, 2001	Barry	712/34	Feb. 23, 2001
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EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO	
A.S.	BH	JP2000029731	Dec. 8, 1999	Japan	G06F 9/38	X
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A.S.	BJ Kikuchi, "Parallelization Assist System", Joho Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.
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A.S.	BL Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.
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RELATED PATENT APPLICATIONS				
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A.S.	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug	Nov. 4, 1999
A.S.	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System	Oct. 2, 1995

DATE CONSIDERED

2/15/06

 EXAMINER

Arpen Sivli Lapan Arndt

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Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT JUL 01 2005 (Use several sheets if necessary)		ATTORNEY DOCKET NO. AUS920030554US1		SERIAL NO. 10/757,249	
APPLICANT DeWitt, Jr. et al.					
FILING DATE January 14, 2004				GROUP ART UNIT 2188-2185	
RELATED PATENT APPLICATIONS					
EXAMINER INITIAL		APPLICATION NO / ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
AS	AA	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
AS	AB	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
AS	AC	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
AS	AD	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
AS	AE	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
AS	AF	10/675,751 / AUS920030482US1	DeWitt, Jr. et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
AS	AG	10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
AS	AH	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
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AS	AM	10/757,192 / AUS920030543US1	DeWitt, Jr. et al.	Method and Apparatus for Providing Pre and Post Handlers for Recording Events	Jan. 14, 2004
AS	AN	10/757,227 / AUS920030555US1	DeWitt, Jr. et al.	Method and Apparatus for Autonomically Moving Cache Entries to Dedicated Storage when False Cache Line Sharing is Detected	Jan. 14, 2004
AS	AO	10/757,197 / AUS920030556US1	DeWitt, Jr. et al.	Method and Apparatus for Optimizing Code Execution Using Annotated Trace Information having Performance Indicator and Counter Information	Jan. 14, 2004
DATE CONSIDERED 2/15/06			EXAMINER Arpan Sarka Arpan Sarka		
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